Notice of References Cited Application/Control No. 10/604,244 Examiner Toan M Le Applicant(s)/Patent Under Reexamination TAI ET AL. Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date - MM-YYYY	Name	Classification
	Α	US-5,761,064	06-1998	La et al.	700/110
	В	US-6,708,073	03-2004	Heavlin, William D.	700/121
	С	US-6,477,432	11-2002	Chen et al.	700/51
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	Н	US-			
	1	US-			
	J	US-			
	К	US-			
	L	US-			
	М	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	0					
	Р					
	σ	-				
	R				•	
	S					
	Т			-		

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Fan et al., Data Mining and Fault Diagnosis Based on Wafer Acceptance Test Data and In-line Manufacturing Data, IEEE 2001, Pages 171-174.
	V	Scher et al., In-line Statistical Process Control and Feedback for VLSI Integrated Circuit Manufacturing, IEEE 1989, Pages 70-75.
	w	Nurani et al., In-line Defect Sampling Methodology in Yield Management: An Integrated Framework, November 1996, IEEE Transactions on Semiconductor Manufacturing, Vol., 9, No. 4, Pages 506-517.
	х	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.